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Ade, M.; Lauwereins, R.; Peperstraete, J.A.;

Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Workshop
7-9 Jun 1995

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2 GRAPE: a CASE tool for digital signal parallel processing

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ASSP Magazine, IEEE [see also IEEE Signal Processing Magazine] , Volume: 7 Is
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[\[Abstract\]](#) [\[PDF Full-Text \(1108 KB\)\]](#) **IEEE JNL**

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4 Buffer memory requirements in DSP applications

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Rapid System Prototyping, 1994. 'Shortening the Path from Specification to Prot
Proceedings., Fifth International Workshop on , 21-23 Jun 1994